OpenCores Coding Guidelines

Rev. 1.2

July 14, 2003

OpenCores

3.2.1. Recommendation: Use asynchronous active high reset.

Using asynchronous reset could result in a smaller core. Using an active high reset makes the core compatible with wishbone spec.

3.2.2. Recommendation: At reset time, all bi-directional ports should be in input state.

Scan expects this and it prevents X values.

3.3. Clocks

3.3.1. Strong Recommendation: Signals that cross different clock domains should be double sampled after crossing domains (double sampling is a MUST). See synchronizer_flop in OpenCores CVS in module common.

Prevents meta-stability state.

3.8. Core I/O ports

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