- Ord]r(0)ing GuideTJı˝/F1 1 Tfı˝0.11.8554 -2.746TDı˝0.-00 13cı˝-0ASAK1(452)-(])0A-7(E)VF E0

PIN/FUNCTION

No.	Pin Name	I/O	Function
1	VREFH	I	Positive Voltage Reference Input Pin, VA Used as a positive voltage reference by ADC & DAC. VREFH is connected externally to filtered VA.
2	VREFL	I	Negative Voltage Reference Input Pin, AGND Used as a negative voltage reference by ADC & DAC. VREFL is connected externally to AGND.

ABSOLUTE MAXIMUM RATINGS

ANALOG CHARACTERISTICS

Parameter			min	typ	max	Units	
Power supply Current		VA=VD=5V					
Analog	AD+DA	PWAD="H",PWDA="H"		41	62	mA	
VA	AD	PWAD="H",PWDA="L"		17	26	mA	
	DA	PWAD="L",PWDA="H"		25	38	mA	DH"

OPERATION OVERVIEW

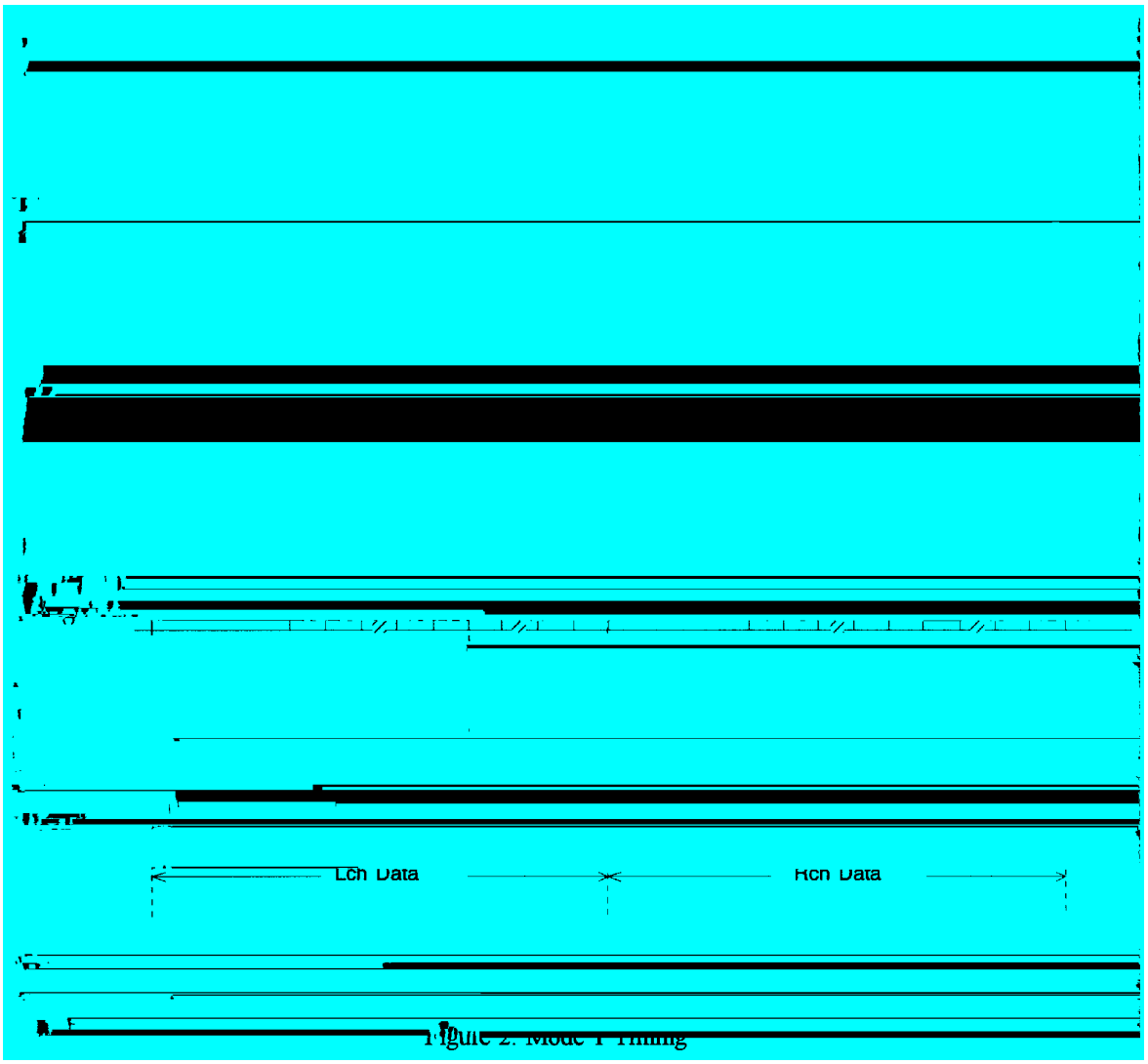

Figure 2. Mode 1 Timing

Digital High Pass Filter

4. Analog Outputs

The analog outputs are also single-ended and centered around the VCOM voltage. The input signal range scales with the supply voltage and nominally 0.626 x (VREFH-VREFL) Vpp. The DAC input data format is 2's complement. The output voltage is a positive full scale for 7FFFFH(@20bit) and a negative full scale for 80000H(@20bit). The ideal output is VCOM voltage for 00000H(@20bit). The internal switched-capacitor filter and continuous-time filter removes most of the noise generated by the delta-sigma modulator of DAC beyond the audio passband.

DC offsets on analog outputs are eliminated by AC coupling since DAC outputs have DC offsets of a few mV.

IMPORTANT NOTICE

- These products and their specifications are subject to change without notice. Before considering any use or application, consult the Asahi Kasei Microsystems Co., Ltd. (AKM)