bit configuration can be stored in any block. This code is copied to and executed from system

3.2 Output Disable

With OE# at a logic-high level (V_{IH}), the device

E

Table 3. 5 Volt F

Command	Scaleable or Basic Command Set[14]	Bus Cycles Req'd	Notes	First Bus Cycle	Second Bus Cycle

4.2.4 CFI QUERY IDENTIFICATION STRING

The Identification String provides verification that the component supports the Common Flash Interface specification. Additionally, it indicates which version of the spec and which vendor-specified command set(s) is (are) supported.

Table 8. CFI Identification

Offset	Length (Bytes)	Description	28F320/160S5E

intel®

4.2.6 DEVICE GEOMETRY DEFINITION

This field provides critical details of the flash device geometry.

Table 10. Device Geometry Definition

Offset	Length (bytes)	Description	28F160S5	28F320S5

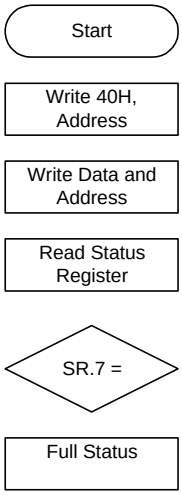
Table 11. Primary-Vendor Specific Extended Query (Continued)

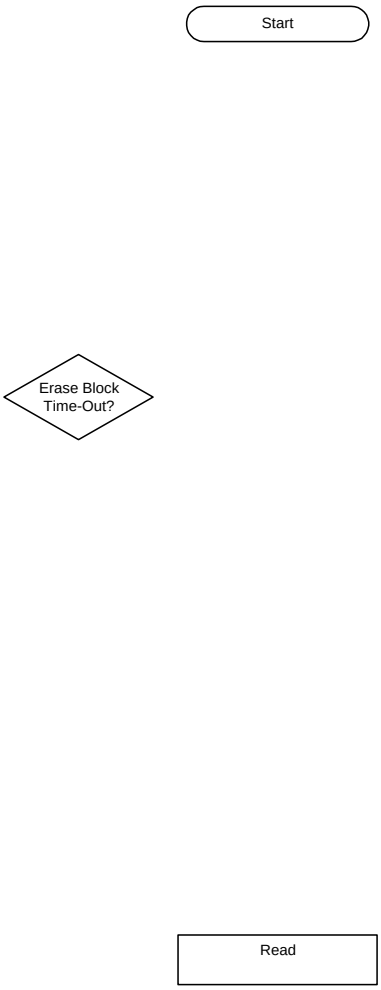
Offset	Length (bytes)	Description	DataE

ition

BWSS	DPS	R

PRELIMINARY


Start
Write 40H, Address
Write Data and Address
Read Status Register
SR.7 =
Full Status


Start
Erase Block
Time-Out?
Read

6.0 ELECTRICAL SPECIFICATIONS

6.1 Absolute Maximum Ratings

Temperature under Bias
Commercial ..... 0 °C to +70 °C
Extended ..... –40 °C to +85 °C

Storage Temperature..... –65 °C to +125 °C

Voltage On Any Pin
(except V_{CC} and V_{PP})
..... –0.5 V to + V_{CC} +0.5 V(1)

V_{CC} Supply Voltage ..... –0.2 V to + V_{CC}+0.5 V(1)

V_{PP} Update Voltage during

6.3 Capacitance [1]

T_A = +25 °C, f = 1 MHz

Symbol	Parameter	Typ	Max	Unit	Condition

6.4 DC Characteristics (Continued)

T_A = –40 °C to +85 °C (Extended) and T_A = 0 °C to +70 °C (Commercial)

Sym	Parameter	Notes	Typ	Max	Unit	Conditions

6.4 DC Characteristics (Continued)

T_A = –40 °C to +85 °C (Extended) and T_A = 0 °C to +70 °C (Commercial)

Sym	Parameter	Notes	Min	Max	Unit	Conditions
V_{IL}	Input Low Voltage	7	–0.5	0.8	V	
V_{IH}	Input High VoltageInpreı335.6 -0.6 reı˝fı˝506.8d5(Inpr8 reı˝fı˝305.58.6reı˝fı˝1˝(IH)Tjı˝/F6 1 Tfı346 682.05 691 Tfı˝6C 655.˝fıı˝fı˝39					

6.6 AC Characteristics—Write Operations[1, 6]

T_A = –40 °C to +85 °C (Extended) and T_A = 0 °C to +70 °C (Commercial)

Versions[6]	5 V ± 5% 5 V ± 10% Vded) and T

W3

STS (R) V_{IH} / V_{IL}

RP# (P)

6.7 Erase, Write, and Lock-Bit Configuration Performance[3, 4]

5 V ± 5%, 5 V ± 10% V_{CC}

Version	5 V V